# PCB-ready breakout boards: Bridging the gap between electronics prototyping and production

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Electronics prototyping using breakout boards allows designers with and without an engineering background to rapidly create interactive prototypes. However, when it comes to transition to a production-ready PCB design, stagnation exists due to the high skill floor required for PCB design. While PCB design automation has been used successfully in recent research tools to reduce the required expertise, little has been done to integrate these tools directly into the electronics prototyping cycle. This position paper aims to bring attention to the possibility of integrating recent PCB design automation paradigms into the electronics prototyping cycle for the creation of PCB-ready breakout boards: breakout boards whose designs would have the ability to be pipelined directly into new user interfaces that leverage the use of automation for the rapid creation of production-ready PCB designs.

 $\texttt{CCS Concepts:} \bullet \textbf{Human-centered computing} \rightarrow \textbf{User interface toolkits;} \bullet \textbf{Hardware} \rightarrow \textbf{PCB design and layout.}$ 

Additional Key Words and Phrases: Breakout Boards, PCB design, Automation, User Interfaces

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#### 1 BACKGROUND

Prototyping using breakout boards is one of the three most popular paradigms when it comes to prototyping with electronic components [11]. Breakout boards consist of a set of modular PCBs, each containing multiple electronic components, which can be easily interconnected using breadboards for interactive circuit design exploration or debugging. Anyone can design and distribute breakout boards without affecting the compatibility between them, popular distributors include Arduino [4], Sparkfun [6], and Adafruit [1].

After circuit prototyping, designers transfer the physical connections made between the breakout boards into a virtual representation format known as an electronic schematic. Electronic schematics provide designers with a simpler visual interactive format for connecting electronic components, where even dimensional blocks are used instead of irregular electronic component footprint shapes. After the schematic design, designers create a PCB layout by drawing virtual copper trace lines between the footprints of the electronic components. Finally, after generating the schematic and PCB layout of the circuit, the circuit design is considered ready for manufacturing. Software tools that assist designers in creating schematics and PCB layouts are known as PCB design tools, examples include KiCad [10], Altium [2], Fusion 360 [5].

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Breakout board prototyping and PCB design require different techniques and design skills and are therefore considered separate stages, being PCB design exponentially more difficult for beginners, making this transition unachievable for many. However we believe there are new opportunities to bring breakout board prototyping closer to PCB design. In this position paper, we explain the challenges of transition between breakout boards and PCB design, we propose our vision and differentiate it from related work, mention some research opportunities, and end with initial work in this direction.

## 2 TRANSLATING BREAKOUT BOARD DESIGN PROTOTYPES TO PCB DESIGNS

Moving from an electronic prototype based on breakout boards to a PCB design is, in most cases, a desired step and often influences the prototyping platforms that are chosen. A recent user study shows that the ability of prototyping platforms to evolve to a custom PCB is the third most important feature for the electronics engineer, in addition to the platform's ease of use and the ability to make changes quickly [11]. While the ease of translating a prototype design to a PCB design is desirable, it is not easy, and to improve the translation process it is first necessary to identify the challenges.

Through the teaching of various courses where students with no previous experience in electronics learn circuit design, we have identified some of the challenges when translating electronic prototypes using breakout boards to PCB designs, which can be summarized as follows: (1) A steep learning curve required to use PCB design tools (e.g., Altium, Fusion 360, KiCad), (2) Understanding each of the components on the breakout boards and separating those required for the final design.

While (1) is clear (2) is not a well-studied problem. In addition to learning PCB design tools, after electronics prototyping, designers have to figure out how to correctly transfer the designs from each breakout board in the prototype design to a PCB design. This involves separating non-important components within the breakout boards (e.g., power regulators, optional configurations, etc.), reviewing all data sheets, redrawing the schematics and PCB layouts, and merging designs. A process that is often complex for beginners and tedious for experts. As an analogy, this would be like integrating a code library into a programming code by going through each method manually and extracting the parts of the code to use. In this process, we believe that there are alternative solutions.

# 3 PCB-READY BREAKOUT BOARDS VISION

In the future, we anticipate that breakout boards will be ready to be incorporated into PCB design automation tools that can automatically generate the schematic and PCB layout of prototype designs with minimal effort. Reducing the gap between electronic prototyping and PCB design. This will allow designers, engineers, makers and researchers to easily transition from prototype ideas to products. We believe that the impact of these types of tools would be significant. Making more ideas that normally remain in prototypes to electronic designs closer to commercial products, thereby bringing more innovation to society.

For this idea to be feasible, subtle design changes must be made to the schematics that ship with the breakout boards in order to integrate them into automated PCB design tools. We call breakout boards that are ready to be integrated into PCB layout automation tools as **PCB-ready breakout boards**.

PCB-ready breakout boards designs will be incorporated into new user interfaces to automatically generate complete production-ready PCB layouts, instead of the traditional consuming process of copying and redesigning each breakout board design to be incorporated into the circuit design. These new interactive interfaces will range from a simple

selection interface of breakout boards to use to more advanced interfaces that allow for multiple design configurations (e.g., I/O pin selections, design constraint validations, part replacements, etc.).

Several existing approaches have realized part but not all of this vision. Commercial tools such as Sparkfun's À La Carte (ALC) [7] and Altium's Upverter Modular [3] have taken small steps to bridge the gap between prototype and production. These tools allow designers to select different breakout boards, development boards or pre-tested circuit blocks, and allow their integration to create complete PCB designs. However these tools are non-automated, and require one or more engineers to manually create the full schematic and PCB layout design. A process with a design fee that can approach \$1,000 per design [8]. In addition, not having standards for scaling the number of usable modules prevents community collaboration, limiting the designs that can be achieved with these tools.

Compared to Sparkfun ALC and Altium Upverter, the tools we envision will be fully automated, with no additional engineering services required, allowing for rapid design iterations. Also, if the process is standardized, it will allow for community collaboration, without limiting the number of designs to a few.

Recent research tools have explored new circuit design approaches that take advantage of automation to rapidly create schematic and PCB layout designs. Echidna [13], Appliancizer [9], Polymorphic Blocks [12] are examples of PCB design automation tools that leverage automation to explore new design paradigms with the goal of facilitating design by increasing design abstraction. Echidna enables the automatic generation of ready-to-manufacturer PCBs from virtual modular schematic designs. In Echidna, the design is abstracted to the selection of components to be used, with that information the software generates a complete PCB layout output. Appliancizer, goes further, abstracting the design to the GUI level, allowing automated PCB layout, firmware, and interface simulation generation from web interfaces. Polymorphic Blocks takes a different approach by allowing automated generation of schematic designs from hardware description languages.

While these research tools allow for automated generation of schematics and PCB layouts from higher levels of abstraction, these tools are not integrated into the physical electronic prototyping process cycle. However, similarities exist between breakout boards and the research tools described above. Both approaches use a modular or blocks design approach for ease of design. While breakout boards present modular circuit designs in a physical format, PCB layout automation research tools use libraries containing virtual modular circuit designs for design automation. These similarities make integration between the two design cycles not a far off possibility.

In contrast, Echidna, Appliancizer, and Polymorphic Blocks, whose libraries and module design are virtual and therefore cannot be easily tested separately, PCB-ready breakout boards will enable both: a physical exploration of designs and a rapid transition to production-ready electronic designs. Furthermore, we envision the encapsulation of information within breakout board designs to be easy and scalable and not something that requires in-depth knowledge (e.g., learning new programming languages, building large databases) as in previous research tools.

## **4 RESEARCH OPPORTUNITIES**

In this vision, there are still many research questions to be answered, some of which are the following:

- What is the optimal way to include additional parameters into breakout board designs for use by PCB design automation tools?
- How to design the user interfaces to include multiple settings and at the same time be easy to use?
- How to allow multiple design configurations required for each module (e.g., adding or removing resistors to set I2C address)?

• How to include validations so that the generated PCB design is safe of design errors?

We also see necessary a user study to correctly identify transition difficulties between prototyping and PCB design.

#### 4.1 Further challenges and opportunities

With this vision we also anticipate possible challenges that the tools may have, some of which are the following.

Obsolete Electronic Components - In the proposed system, the components used on the breakout boards will be the same as those used in the final PCB design. Faced with an obsolete electronic component, the design would not be possible. Methods of replacing obsolete electronic components or rapidly updating breakout boards designs are necessary.

PCB Optimizations - In the proposed system, breakout board PCB layouts would be automatically copied and pasted, and the missing nets would be routed automatically for ease of design. In this approach, the final PCB layout would lack potential routing optimizations compared to a manual layout design. However, we see that in the future PCB auto-routing improvements could efficiently layout the complete PCB.

## 5 COMMERCIALIZATION OPPORTUNITIES

Breakout boards or integrated toolkits that adopt a PCB-ready design might become more attractive to users compared to standard breakouts boards, as the user has the added benefit of ease of translation from prototype to product.

#### **6 WORK IN THIS DIRECTION**

Our team has started taking steps in this direction towards PCB-ready breakout boards. Our current system allows annotations on existing schematics to create schematic blocks. These schematic blocks can be imported into a simplified user interface that validates the connections and generates a complete schematic. The next step is to generate the PCB layout from blocks which has already been demonstrated in previous tools.

# 7 RESEARCH TEAM

J. Garza is a Ph.D. student at University of California, San Diego, where he explores new methods to facilitate and accelerate the design of electronic devices by creating computer-aided design (CAD) tools with new workflows and interactions.

Steven Swanson is a professor in the Department of Computer Science and Engineering at the University of California, San Diego and the director of the Non-volatile Systems Laboratory. He has received an NSF CAREER Award, Google Faculty Awards, a Facebook Faculty Award, and been a NetApp Faculty Fellow. He is a co-founder of the Non-Volatile Memories Workshop.

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